

FEATURES:

- Meet the pulse waveform template of CCITT I.430 when recommended transformer and chip pair are used
- Developed for enhanced EMC performance
- Excellent longitudinal balance
- Low or high frequency choke options available

TEST CONDITIONS:

- Main Transformers Inductance -----10KHz/100mV
- CMC Inductance -----100KHz/20mV
- Leakage Inductance & Interwinding Capacitance – 100KHz/100mV



YUAN DEAN SCIENTIFIC



ISDN-S0 Interface
Transformers
49TM SERIES
Surface Mount
2KVrms Isolation
Dual



Electrical Specifications@25°C

Part Number	Turns Ratio (Line: Chip) (±2%)	Line Side Inductance (mH min.)	Interwinding Capacitance (pF max)	Line Side Leakage Inductance (uH max)	DCR Line Side (Ω ±15%)	DCR Line Side (Ω ±15%)	Schematics
49TM-3031**ANL	1CS: 1CS	30	100	5	2.9	2.8	A
49TM-3032**BNL	1CT: 1CT	30	100	5	2.9	2.8	B
49TM-3033**ANL	1CS: 2CS	30	100	5	3.0	5.4	A
49TM-3034**BNL	1CT: 2CT	30	100	5	3.0	5.4	B
49TM-3035**ANL	1CS: 2.5CS	30	100	5	3.0	6.8	A
49TM-3036**BNL	1CT: 2.5CT	30	100	5	3.0	6.8	B

www.yds.com.tw


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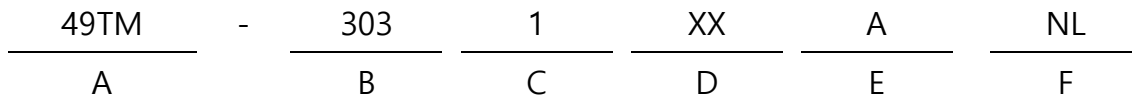
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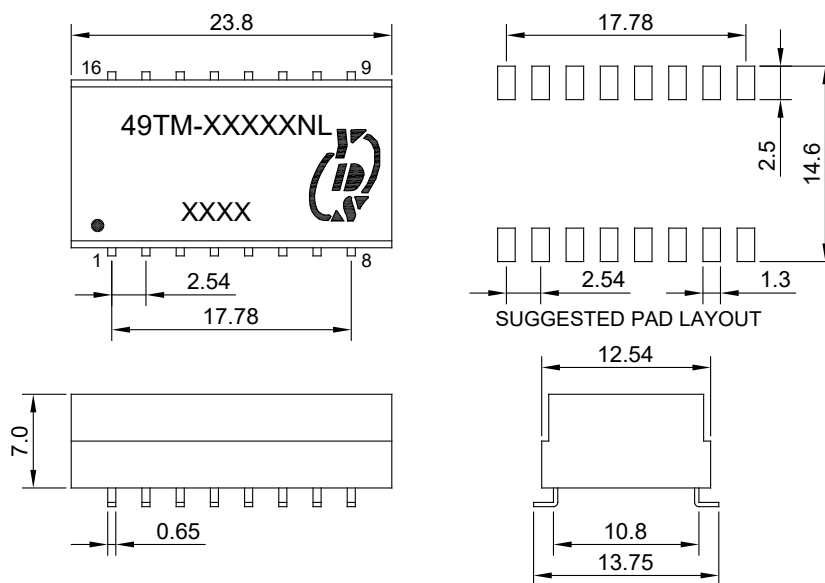
Part Number	Common Mode Choke									
	-00		-51		-12		-52		-53	
	OCL (uH)	DCR (Ω max)	OCL (uH)	DCR (Ω max)	OCL (uH)	DCR (Ω max)	OCL (uH)	DCR (Ω max)	OCL (mH)	DCR (Ω max)
49TM-3031**ANL	None		50	0.4	100	0.6	500	1.4	5.0	2.0
49TM-3032**BNL	None		50	0.4	100	0.6	500	1.4	5.0	2.0
49TM-3033**ANL	None		50	0.4	100	0.6	500	1.4	5.0	2.0
49TM-3034**BNL	None		50	0.4	100	0.6	500	1.4	5.0	2.0
49TM-3035**ANL	None		50	0.4	100	0.6	500	1.4	5.0	2.0
49TM-3036**BNL	None		50	0.4	100	0.6	500	1.4	5.0	2.0

Part Number



- A : Series
- B : Line Side Inductance
- C : Sequence id
- D : Inductance and DCR
- E : Schematic
- F : RoHS Version

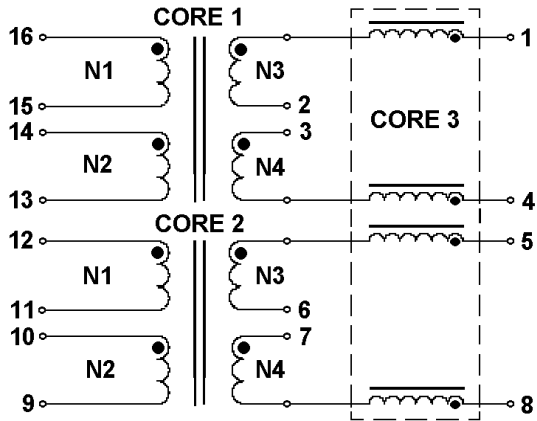
Mechanicals and Dimensions



UNIT: mm
 Unless otherwise specified, all tolerances are ±0.25

Schematics

A



B

